

## ABSTRACT

A driving circuit for an active matrix electroluminescence device (AMELD) can control an output current value according to R/G/B channels by receiving a digital signal of n bits. In the driving circuit of the AMELD having data and gate drivers that respectively transmit a data signal and a scan signal to each pixel region, the data driver includes a latch for latching a control signal temporarily stored, and a plurality of digital to analog converters (DAC) for outputting a reference current of a certain level as a data signal according to R/G/B channels is latched by the control signal.